



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): ITO et al.	Atty. Dkt.: 01-560
Serial No.: 10/786,136	Group Art Unit: 2123
Filed: February 26, 2004	Examiner: JANAKIRAMAN, NITHYA
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND MICRO-COMPUTER DEVELOPMENT ASSISTING APPARATUS	

Commissioner for Patents
Alexandria, VA 22313-1450

Date: July 31, 2008

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the references listed on the attached Form PTO-1449 are being brought to the attention of the Examiner without any admission that they constitute statutory prior art, or without any admission that they contain subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

Further, pursuant to 37 C.F.R. §1.97(e), the undersigned hereby certifies that each listed reference was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

As a FINAL Office Action on the merits has been mailed in the above identified application, the fee due under 37 C.F.R. 1.17(p) is attached. Please charge any additional fee to Applicant's attorney's Deposit Account No. 50-1147.

Also, the Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed references have been considered and made of record.

Respectfully submitted,

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Reg. No. 37,701

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